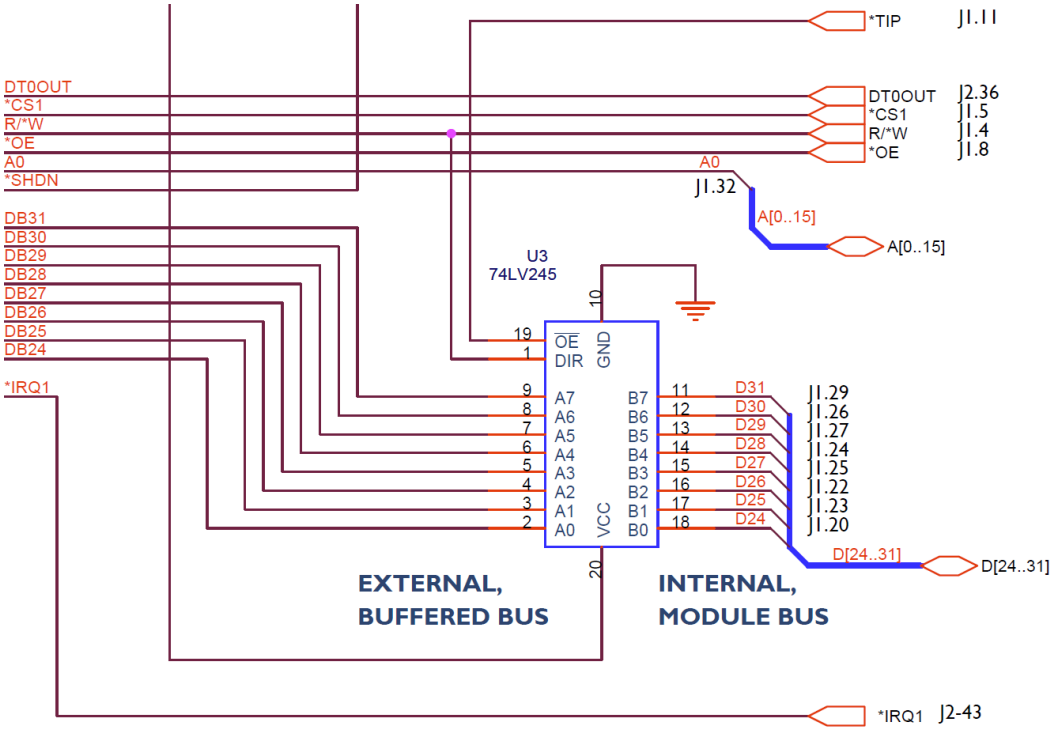

*TIP
J1.11
DT0OUT
*CS1
R/*W
*OE
A0
*SHDN
DT0OUT
*CS1
R/*W
*OE
J2.36
J1.5
J1.4
J1.8
A0
J1.32
A[0..15]
A[0..15]
DB31
DB30
DB29
DB28
DB27
DB26
DB25
DB24
U3
74LV245
10
19
1
OE
DIR
GND
9
8
7
6
5
4
3
2
A7
A6
A5
A4
A3
A2
A1
A0
B7
B6
B5
B4
B3
B2
B1
B0
VCC
11
12
13
14
15
16
17
18
D31
D30
D29
D28
D27
D26
D25
D24
J1.29
J1.26
J1.27
J1.24
J1.25
J1.22
J1.23
J1.20
D[24..31]
D[24..31]
20
EXTERNAL,
BUFFERED BUS
INTERNAL,
MODULE BUS
*IRQ1
*IRQ1
J2-43